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*Admitted only in Maryland
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*Practice limited to
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October 24, 2008

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Re: U.S. Utility Patent No. 7,395,468; Issue Date: July 1, 2008
(from Appl. No. 10/806,093; Filed: March 23, 2004)
For: **Methods for Debugging Scan Testing Failures of Integrated Circuits**
Inventor: Amar GUETTAF
Your Ref: BU3586
Our Ref: 1875.4450000

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Request for Certificate of Correction under 37 C.F.R. § 1.322;
2. One (1) page of Certificate of Correction Form (PTO/SB/44); and
3. One (1) return postcard.

The above-listed documents are filed electronically through EFS-Web.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

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MDS:mlb
Enclosures
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